Refine Search

Search Results -

Terms	Documents
L27	0

US Pre-Grant Publication Full-Text Database

US Patents Full-Text Database

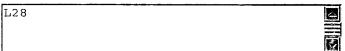
US OCR Full-Text Database

Database:

EPO Abstracts Database JPO Abstracts Database Derwent World Patents Index

IBM Technical Disclosure Bulletins

Search.











Search History

DATE: Thurs Tay, January 27, 2005 Printable Copy Create Case

Set Name Query side by side	<u>Hit</u> <u>Count</u>	Set Name result set
DB=TDBD; PLUR=YES; OP=OR		
<u>L28</u> L27	0	<u>L28</u>
DB=DWPI; $PLUR=YES$; $OP=OR$		
<u>L27</u> L26	0	<u>L27</u>
DB=JPAB; $PLUR=YES$; $OP=OR$		
<u>L26</u> L25	0	<u>L26</u>
DB=EPAB; $PLUR=YES$; $OP=OR$		
<u>L25</u> L24	0	<u>L25</u>
DB=USOC; PLUR=YES; OP=OR		
<u>L24</u> L23	0	<u>L24</u>
DB=USPT; $PLUR=YES$; $OP=OR$		
<u>L23</u> L22	0	<u>L23</u>
DB=PGPB; PLUR=YES; OP=OR		
<u>L22</u> L21	0	<u>L22</u>

DB=	USPT; P1 UR=YES; OP=OR		
<u>L21</u>	"double gate CMOS" and "trench gate"	0	<u>L21</u>
<u>L20</u>	"double gate CMOS"	11	<u>L20</u>
<u>L19</u>	61976?2	2	<u>L19</u>
<u>L18</u>	5899710	4	<u>L18</u>
<u>L17</u>	L16	0	<u>L17</u>
<u>L16</u>	"trench gate" same (("first gate" near5 (polysilicon)) same ("second gate" near5 (polysilicon)))	0	<u>L16</u>
DB=	TDBD; PLUR=YES; OP=OR		
<u>L15</u>	L14	0	<u>L15</u>
DB = 0	DB=JPAB; FLUR=YES; OP=OR		
<u>L14</u>	L13	0	<u>L14</u>
DB = 1	EPAB; PLUR=YES; OP=OR		
<u>L13</u>	L12	0	<u>L13</u>
DB =	USOC; PLUR=YES; OP=OR		
<u>L12</u>	L11	0	<u>L12</u>
DB =	USPT; PLUR=YES; OP=OR		
<u>L11</u>	"trench gate" same (("first gate" near5 (N near3 polysilicon)) same ("secone gate" near5 (P near3 polysilicon)))	0	<u>L11</u>
<u>L10</u>	"trench gate" same ("first gate" near5 "n type") same ("second gate" near5 "p type")	0	<u>L10</u>
DB=TDBD; $PLUR=YES$; $OP=OR$			
<u>L9</u>	L8	0	<u>L9</u>
DB = 1	DB=DWPI; PLUR=YES; OP=OR		
<u>L8</u>	L7	0	<u>L8</u>
DB = 0	JPAB; PLUR=YES; OP=OR		
<u>L7</u>	L6	0	<u>L7</u>
DB = 0	EPAB; PLUR=YES; OP=OR		
<u>L6</u>	L5	0	<u>L6</u>
DB = 1	PGPB; PLUR=YES; OP=OR		
<u>L5</u>	L4	0	<u>L5</u>
DB =	DB=USOC; $PLUR=YES$; $OP=OR$		
<u>L4</u>	L3	0	<u>L4</u>
DB=USPT; PLUR=YES; OP=OR			
<u>L3</u>	L2 and ("first gate" near6 fermi) and ("second gate" near5 fermi)	0	<u>L3</u>
<u>L2</u>	"trencle gate" same (multigate or double gate)	1115	<u>L2</u>
<u>L1</u>	658629C	3	<u>L1</u>

END OF SEARCH HISTORY